**Time Table 2023(Even Semester)**

**BTech Semester IV Electronics& Communication Engineering**

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| Day | 900-1000 | 1000-1100 | 1100-1200 | 1200-1300 | 1-2 PM | 1400-1500 | 1500-1600 | 1600-1700 |
| M | EC232, Digital Electronics Circuit Lab(0-0-3-3), N.Debala, Devyani | Library | LUNCH | MA202,Probability and Random Process (3-0-0-6), Dr. Sunil Pandey  | EC202, Digital Electronics Circuits(3-0-0-6),Richard N | EC206, EEMI (3-0-0-6),Dr L Surjakumar Singh |
| T | EC234, Analog Communication Lab (0-0-3-3), Mr. S. Nonglenkhomba Meitei, Robert okram | Library | MA202,Probability and Random Process (3-0-0-6), Dr. Sunil Pandey  | EC204, Analog Communication(3-0-0-6),Dr. Kh. Jilenkumari Devi | EC206, EEMI(3-0-0-6), Dr L Surjakumar Singh |
| W | EC236Electronics and Electrical Measurement &Instrument Lab(0-0-3-3), Mrs H. Anupa | Library | MA202,Probability and Random Process (3-0-0-6), Dr. Sunil Pandey  | EC204, Analog Communication(3-0-0-6),Dr. Kh. Jilenkumari Devi | EC208, Signals System Analysis(3-0-0-6), Dr. K. Pritamdas |
| T | Library | Library | ECE202, Digital Electronics Circuits (3-0-0-6), Richard N | EC204, Analog Communication(3-0-0-6),Dr. Kh. Jilenkumari Devi | EC208, Signals System Analysis(3-0-0-6), Dr. K. Pritamdas |
| F | SA202,NSS/Sport(0-0-2-0), Thankin | Library | ECE202, Digital Electronics Circuits(3-0-0-6), Richard N | EC206, EEMI (3-0-0-6), Dr L Surjakumar Singh | EC208, Signals and Systems(3-0-0-6), Dr. K. Pritamdas  |

**BTech Semester VI Electronics and Communication Engineering**

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| Day | 900-1000 | 1000-1100 | 1100-1200 | 1200-1300 | 1-2 PM | 1400-1500 | 1500-1600 | 1600-1700 |
| M | Library | EC310, VHDL (3-0-0-6), Dr Manoj Kumar | EC304, Embedded system (3-0-0-6), Dr. L. Surajkumar Singh | EC308, Antenna Design (3-0-0-6), S Jayananda Singh | LUNCH | EC332, VLSI & VHDL Lab (0-0-3-3),Dr. Manoj Kumar,Y Shantilkumar |
| T | Library | EC310, VHDL(3-0-0-6), Dr Manoj Kumar | EC306, Industrial Electronics(3-0-0-6), Dr. Kh. Jilenkumari Devi | EC308, Antenna Design (3-0-0-6), S Jayananda Singh | EC334, Embedded System Lab (0-0-3-3),Dr L SurajKumar Singh, Mr. S. Nonglenkhomba Meitei  |
| W | Library | EC310, VHDL (3-0-0-6), Dr Manoj Kumar  | EC306, Industrial Electronics(3-0-0-6), Dr. Kh. Jilenkumari Devi | EC302, VLSI Design (3-0-0-6), Dr Tarun | EC336, Industrial Electronics Lab (0-0-3-3),Mrs H. Anupa, Mrs N Debala |
| T | Library | EC304, Embedded system (3-0-0-6), Dr. L. Surajkumar Singh | EC306, Industrial Electronics, (3-0-0-6), Dr. Kh. Jilenkumari Devi | EC302, VLSI Design (3-0-0-6),Dr Tarun | EC338, Antenna Design Lab (0-0-3-3), Dr. Kalyan Mondal,Robert Okram |
| F | Library | EC304, Embedded system (3-0-0-6), Dr. L. Surajkumar Singh | EC308, Antenna Design (3-0-0-6), S Jayananda Singh | EC302, VLSI Design (3-0-0-6), Dr Tarun | Library |

**BTech Semester VIII Electronics and Communication Engineering**

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| Day | 900-1000 | 1000-1100 | 1100-1200 | 1200-1300 | 1-2 PM | 1400-1500 | 1500-1600 | 1600-1700 |
| M | Library | EC 462,Wireless and Mobile communication(3-0-0-6),Dr A. Dinamani Singh | EC 474,Fiber Optics Communication(3-0-0-6), Mr. Th Churchil Singh | EC422, Project Work-II (0-0-9-9) | LUNCH | EC422, Project Work-II (0-0-9-9) |
| T | Library | EC 462,Wireless and Mobile Communication(3-0-0-6),Dr A. Dinamani Singh | EC484, Semiconductor IC Technology (3-0-0-6), Dr Tarun | EC422, Project Work-II(0-0-9-9) | EC422, Project Work-II (0-0-9-9) |
| W | EC 474,Fiber Optics Communication(3-0-0-6), Mr. Th Churchil Singh | EC 462,Wireless and Mobile Communication(3-0-0-6),Dr A. Dinamani Singh | EC484, Semiconductor IC Technology (3-0-0-6), Dr Tarun | EC422, Project Work-II (0-0-9-9) | EC422, Project Work-II (0-0-9-9) |
| T | HS402,Professional Ethics (2-0-0-4), Diana | EC 474,Fiber Optics Communication(3-0-0-6), Mr. Th Churchil Singh | EC484, Semiconductor IC Technology (3-0-0-6), Dr Tarun | EC422, Project Work-II(0-0-9-9) | EC422, Project Work-II (0-0-9-9) |
| F | HS402, Professional Ethics(2-0-0-4),Diana | HS402, Professional Ethics (2-0-0-4), Diana | EC422, Project Work-II(0-0-9-9), | EC422, Project Work-II(0-0-9-9) | EC422, Project Work-II (0-0-9-9) |

**MTech Semester II Electronics and Communication Engineering**

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| Day | 900-1000 | 1000-1100 | 1100-1200 | 1200-1300 | 1-2 PM | 1400-1500 | 1500-1600 | 1600-1700 |
| M | EC532, VLSI and Embedded Lab-II (0-0-3-3),Dr. Ashish Ranjan,Mrs Prena Rana**OR**EC534, Advanced Communication Lab(0-0-3-3), Dr A. Dinamani Singh |  | LUNCH | EC 502, Semiconductor IC technology (3-0-0-6),Dr. Ng Chitrlekha**OR**EC594,Image Processing Techniques(3-0-0-6),Dr. Pritamdas | EC506, Advance Microwave Engineering(3-0-0-6), Dr. Kalyan Mondal | EC552, Low Power VLSI(3-0-0-6),Dr. Ashish Ranjan |
| T | EC532, VLSI and Embedded Lab-II (0-0-3-3), Dr. Ashish Ranjan, Mrs Prena Rana**OR** EC534 ,Advanced Communication Lab(0-0-3-3), Dr A. Dinamani Singh |  | EC 502, Semiconductor IC technology (3-0-0-6), Dr. Ng Chitrlekha**OR**EC594,Image Processing Techniques(3-0-0-6),Dr. Pritamdas | EC504, Advanced Digital Communication  (3-0-0-6), Dr A. Dinamani Singh | EC552, Low Power VLSI (3-0-0-6),Dr. Ashish Ranjan  |
| W | EC532, VLSI and Embedded Lab-II (0-0-3-3),Dr. Ashish Ranjan, Mrs Prena Rana**OR**EC534 ,Advanced Communication Lab(0-0-3-3), Dr A. Dinamani Singh |  | EC 502, Semiconductor IC technology (3-0-0-6), Dr. Ng Chitrlekha **OR**EC594,Image Processing Techniques(3-0-0-6),Dr. Pritamdas | EC504, Advanced Digital Communication (3-0-0-6),Dr A. Dinamani Singh | Library |
| T | EC532, VLSI and Embedded Lab-II (0-0-3-3), Dr. Ashish Ranjan, Mrs Prena Rana  |  | EC506, Advance Microwave Engineering (3-0-0-6), Dr. Kalyan Mondal | EC504, Advanced Digital Communication (3-0-0-6) Dr A. Dinamani Singh | Library |
| F |  |  | EC506, Advance Microwave Engineering (3-0-0-6),Dr. Kalyan Mondal | EC552, Low Power VLSI(3-0-0-6), Dr. Ashish Ranjan  | Library |